

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:	)	
Kazuaki <b>GOTO</b> et al.	)	Group Art Unit: 2825
Serial No. 10/811,835	)	
Filed: March 30, 2004	)	Confirmation No. 3051
For: METHOD OF DESIGNING A CIRCUIT	)	Date: March 11, 2008
OF A SEMICONDUCTOR DEVICE	)	
WITH A SPARE UNDERGROUND	)	
CELL	)	

**AMENDMENT UNDER 37 C.F.R. 1.111**

Mail Stop AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Non-Final Office Action mailed December 11, 2007, please amend the above-captioned application, as follows:

**Amendments to the Specification** are reflected in the listing of claims in this paper

**Amendments to the Claims** are reflected in the listing of claims in this paper.

**Remarks** follow the amendment section of this paper.